

IN MEMORIAM

JAMES A. CAUFFMAN

Director, Readiness, Training and Environmental Quality

Office of Naval Research



Mr. Cauffman graduated from George Washington University in 1956 with a degree in Electrical Engineering, and later pursued graduate studies in Engineering Administration at the same school.

In 1956, he joined the Bureau of Ships as a junior engineer, working on modular design concepts for electronics. He reported for active duty with the USAF Security Service in late 1956, and spent three years with the National Security Agency, performing worst-case analysis of novel digital circuits for advanced computers.

He returned to the Bureau of Ships in 1960 and was responsible for developing and executing the Bureau's R&D program in Microelectronics. During this period, he was instrumental in starting the

Microelectronic Laboratory at the NCCOSC (R&D Division), and pioneering in the application of microelectronics to sonar signal processing.

In 1966, he was transferred with the Bureau's Electronic Devices R&D program to the Naval Electronics Systems Command, now the Space and Naval Warfare Systems Command. In 1981 he assumed the position as Head of the Electronic Technology Division in the NAVELIX R&D Directorate. He was responsible for many technologies that supported Navy electronics needs. Some notable efforts include the development of the EMPRESS I, the first Navy facility for evaluating the vulnerability of ships to nuclear EMP; the development of technology base to protect IR systems from damage or interference from RF sources; and the focusing of Navy interests in microwave monolithic integrated circuits that led to the Navy's major contribution to the DARPA MIMIC program.

In September 1975, Mr. Cauffman assumed the position of Technology Area Manager for the Navy's Exploratory Development program in Electronic Devices. In February 1989, he assumed the position of Director, Support Technology, ONT 22, responsible for managing a diverse set of support-technology efforts aimed at improving the Navy's efforts to build better systems, better utilize its manpower, and improve its Command, Control and Communications systems.

Mr. Cauffman played an active role as a member of the Steering Committee of the Government Microcircuit Applications Conference (GOMAC) from 1984 to 1993. In 1985 he served as the GOMAC General Chair.

In 1993, Mr. Cauffman assumed responsibility for the Readiness, Training and Environmental Quality Department of ONR Technology Directorate, which directs a diverse set of integrated S&T programs in support of Navy needs in Manpower Personnel and Training, and Infrastructures.