GOMACTech 2007 Technical Session Proposal and Information Form

Enabling Technology for sub-100nm Rad-Hard Microelectronics		
Enabling technologies are being developed to address the challenges of		
radiation hardening sub-100nm microelectronics to meet requirements		
for future systems. A consortium of government, commercial, and		
university partners have teamed to investigate various hardening		
approaches in addition to bolstering the necessary tools framework to		
achieve and support sub-100nm RH technologies. This session will		
present some of the latest work addressing the enabling technologies for		
sub-100nm RH microelectronics.		
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1		Hap Hughes	NRL/Albany
2		Dave Alexander/Wes Morris	AFRL
3	90nm modeling		VU
4			CFDRC
(shorter			
talk)			
5			Lynguent
(shorter			
talk)			

Paper tiles are not final at this time.